



職位：數位 IC 設計工程師

工作地點：台灣

職位描述：

- 影像處理與影像壓縮設計
- 高速介面 MIPI/ISP

職位要求：

- 研究所以上，電機電子工程或光電工程相關系所畢業
- 2 年以上工作經驗
- 熟悉數位 IC 設計流程與 FPGA 驗證經驗
- 熟面板驅動 IC 設計

We offer competitive remuneration package and excellent career prospects to the right candidate. To apply, please send your full resume with current and expected salary by one of the following channels. Please quote our reference number in your application.

1. Mail: Human Resources Department, Solomon Systech Limited, 6/F, No. 3 Science Park East Avenue, Hong Kong Science Park, Shatin, N.T., or
2. E-mail: recruit@solomon-systech.com
3. Upload the resume directly by clicking the apply button on each job opening

We regret that only short-listed candidates will be notified.